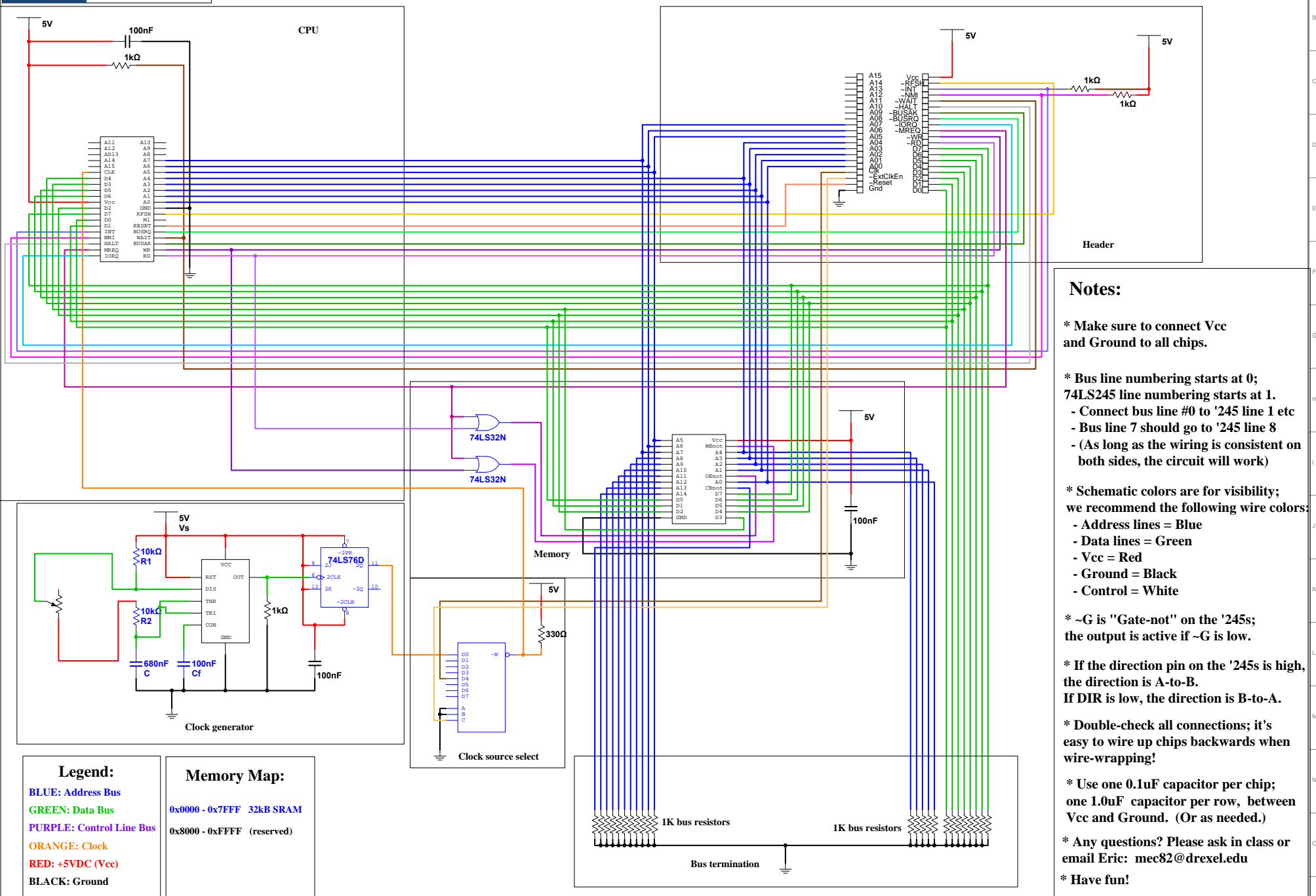




DRaCo / Z80:
DRexel Assembly
Computer
 (8-bit version; core)



Notes:

- * Make sure to connect Vcc and Ground to all chips.
- * Bus line numbering starts at 0; 74LS245 line numbering starts at 1.
 - Connect bus line #0 to '245 line 1 etc
 - Bus line 7 should go to '245 line 8
 - (As long as the wiring is consistent on both sides, the circuit will work)
- * Schematic colors are for visibility; we recommend the following wire colors:
 - Address lines = Blue
 - Data lines = Green
 - Vcc = Red
 - Ground = Black
 - Control = White
- * ~G is "Gate-not" on the '245s; the output is active if ~G is low.
- * If the direction pin on the '245s is high, the direction is A-to-B. If DIR is low, the direction is B-to-A.
- * Double-check all connections; it's easy to wire up chips backwards when wire-wrapping!
- * Use one 0.1uF capacitor per chip; one 1.0uF capacitor per row, between Vcc and Ground. (Or as needed.)
- * Any questions? Please ask in class or email Eric: mec82@drexel.edu
- * Have fun!