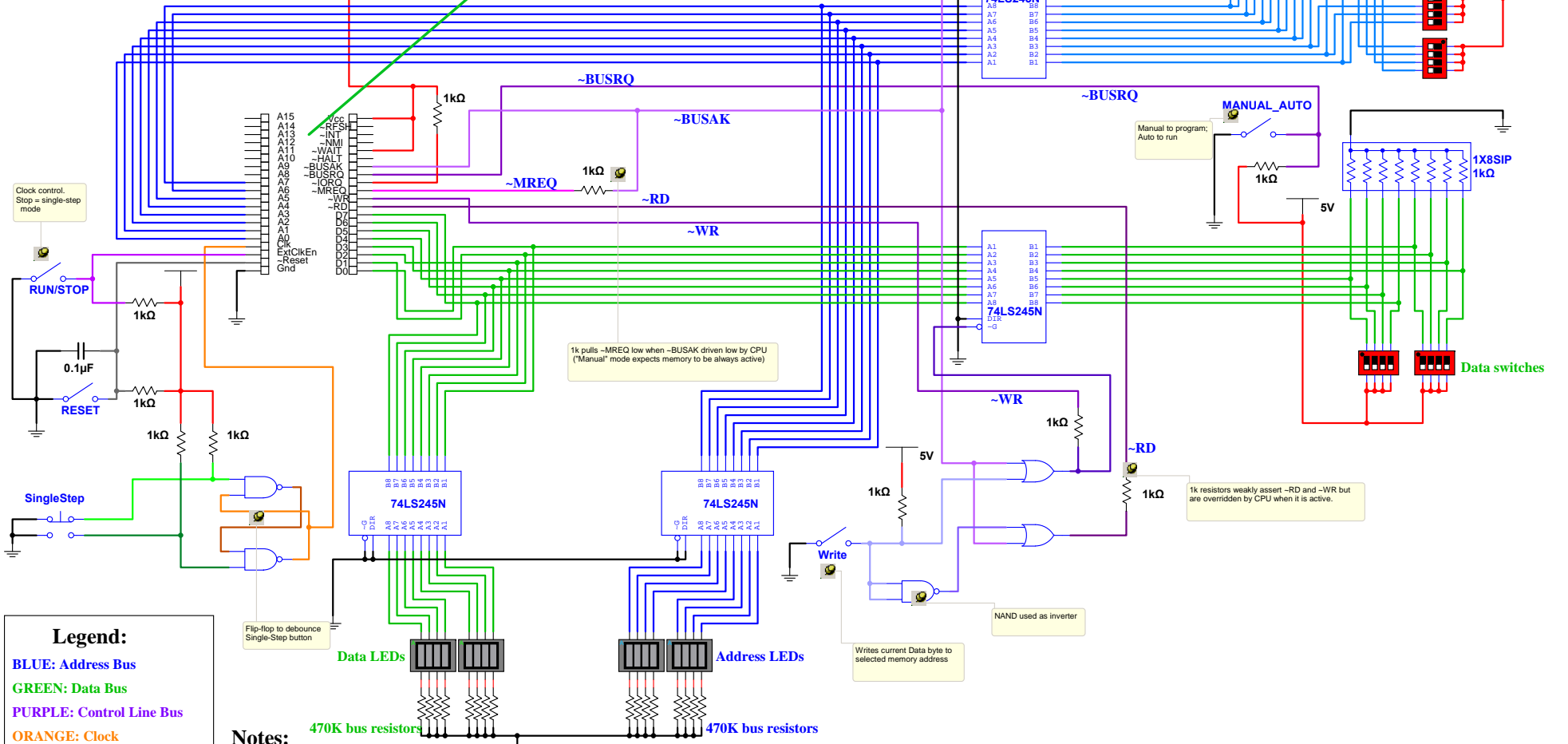




**DRACO:**  
**DRexel Assembly**  
**Computer**  
 (Control panel, 8-bit version)

Header is a single DIP40,  
 wired to control and core circuits



Clock control.  
 Stop = single-step mode

SingleStep

**Legend:**  
 BLUE: Address Bus  
 GREEN: Data Bus  
 PURPLE: Control Line Bus  
 ORANGE: Clock  
 RED: +5VDC (Vcc)  
 BLACK: Ground

**Notes:** 470K bus resistors  
 \* Rotary DIPs are normally-open; use pull-downs to ground / connect switch common / connect switch common to Vcc.  
 \* MSB of rotary switches is on the right (reverse order); verify this when wiring them.

1k pulls -MREQ low when -BUSAK driven low by CPU  
 ("Manual" mode expects memory to be always active)

1k resistors weakly assert -RD and -WR but are overridden by CPU when it is active.

Writes current Data byte to selected memory address

NAND used as inverter

Flip-flop to debounce Single-Step button